



05/13/02

PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPLICATION		FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
APPL NUM	10065045	09/13/2002	331	17	2817	KinKead
**APPLICANTS: Ho Hsu-Feng; Hsu Tse-Hsiang;						
**CONTINUING DATA VERIFIED:						
** FOREIGN APPLICATIONS VERIFIED:						
PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	RESCIND	<input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met			<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO MTKP0019USA		
Verified and Acknowledged Examiners's initials						
TITLE : Phase-locked loop having phase detector error signal reshaping and method thereof U.S.DEP.T. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)						

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
			Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
		Application Examiner		
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